IN THE CLAIMS:

Amendments to the Claims

Please cancel claims 1 and 7 without prejudice or disclaimer of the subject matter thereof, please cancel claims 10, 11, 14, 18-22, 25 and 26, which claims stand withdrawn from consideration, without prejudice or disclaimer of the subject matter thereof and without prejudice to the right to file a divisional application directed thereto, and please rewrite claims 6, 9 and 23 in independent form and add the new claim as shown below.

Listing of Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-5 (canceled)

6. (currently amended) A liquid crystal display apparatus according to elaim 1, having a pair of substrates of which at least one substrate is transparent and a liquid crystal layer sandwiched between the substrates, comprising:

a plurality of scanning electrodes formed on one of the substrates; and
a plurality of signal electrodes intersecting in a matrix form with said plurality
of scanning electrodes;

wherein the display apparatus further comprises, within each of the regions surrounded by said plurality of scanning electrodes and said plurality of signal electrodes:

(a) a display data holding circuit connected to a corresponding scanning electrode and signal electrode, for fetching and storing display data from a signal electrode in response to a scanning signal for holding a display image without

updating the display data while a power supply to the display apparatus is maintained, the display data holding circuit having one of a coplanar and an inverse stagger structure;

(b) a switching device connected to said display data holding circuit and having a switching operation thereof controlled by the display data holding circuit; and

(c) a display electrode connected to said switching device;

wherein said display data holding circuit includes a thin film transistor having a gate connected to the corresponding scanning electrode and one of a drain and a source connected to the corresponding signal-line electrode, and a capacitor at least partially formed by a portion of one of the drain and the source of said thin film transistor.

Claims 7 and 8 (canceled)

9. (currently amended) A liquid crystal display apparatus according to elaim 1, having a pair of substrates of which at least one substrate is transparent and a liquid crystal layer sandwiched between the substrates, comprising:

a plurality of scanning electrodes formed on one of the substrates; and
a plurality of signal electrodes intersecting in a matrix form with said plurality
of scanning electrodes;

wherein the display apparatus further comprises, within each of the regions surrounded by said plurality of scanning electrodes and said plurality of signal electrodes:

(a) a display data holding circuit connected to a corresponding scanning electrode and signal electrode, for fetching and storing display data from a signal electrode in response to a scanning signal for holding a display image without

updating the display data while a power supply to the display apparatus is

maintained, the display data holding circuit having one of a coplanar and an inverse

stagger structure;

(b) a switching device connected to said display data holding circuit and having a switching operation thereof controlled by the display data holding circuit; and

(c) a display electrode connected to said switching device;

wherein said display data holding circuit includes a thin film transistor which has a gate connected to the corresponding scanning electrode and one of a drain and a source connected to corresponding signal electrode, and a static memory circuit connected to the other of the drain and the source of said thin film transistor, the static memory circuit including a plurality of thin film transistors.

Claims 10-22 (canceled)

23. (currently amended) A liquid crystal display apparatus according to claim 1, having a pair of substrates of which at least one substrate is transparent and a liquid crystal layer sandwiched between the substrates, comprising:

a plurality of scanning electrodes formed on one of the substrates; and
a plurality of signal electrodes intersecting in a matrix form with said plurality
of scanning electrodes;

wherein the display apparatus further comprises, within each of the regions surrounded by said plurality of scanning electrodes and said plurality of signal electrodes:

(a) a display data holding circuit connected to a corresponding scanning electrode and signal electrode, for fetching and storing display data from a signal electrode in response to a scanning signal for holding a display image without

updating the display data while a power supply to the display apparatus is
maintained, the display data holding circuit having one of a coplanar and an inverse
stagger structure;

(b) a switching device connected to said display data holding circuit and having a switching operation thereof controlled by the display data holding circuit; and

(c) a display electrode connected to said switching device;

wherein said display electrode is an opaque reflection electrode arranged in overlapping relationship with at least one of said scanning electrode, said signal electrode and a thin film transistor for enabling driving of the liquid crystal display apparatus in a reflection type display mode.

24. (previously presented) A liquid crystal display apparatus according to claim 23, wherein said liquid crystal layer is a guest-host type liquid crystal.

Claims 25 and 26 (canceled)

27. (new) A liquid crystal display apparatus according to claim 6, wherein one electrode of the capacitor is formed of a same material as a material of one of the drain and the source of said thin film transistor.